



| TFT PIN DESCRIPTION | |
|---------------------|--------|
| PIN | SYMBOL |
| 1 | LEDK |
| 2 | LEDA |
| 3 | GND |
| 4 | VDD |
| 5 | R0 |
| 6 | R1 |
| 7 | R2 |
| 8 | R3 |
| 9 | R4 |
| 10 | R5 |
| 11 | R6 |
| 12 | R7 |
| 13 | G0 |
| 14 | G1 |
| 15 | G2 |
| 16 | G3 |
| 17 | G4 |
| 18 | G5 |
| 19 | G6 |
| 20 | G7 |
| 21 | B0 |
| 22 | B1 |
| 23 | B2 |
| 24 | B3 |
| 25 | B4 |
| 26 | B5 |
| 27 | B6 |
| 28 | B7 |
| 29 | GND |
| 30 | PCLK |
| 31 | DISP |
| 32 | HSYNC |
| 33 | VSYNC |
| 34 | DE |
| 35 | NC |
| 36 | GND |
| 37 | NC(XR) |
| 38 | NC(YD) |
| 39 | NC(XL) |
| 40 | NC(YU) |

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE
2. OPERATING VOLTAGE: VDD=3.3V
3. VIEWING DIRECTION: 12 O'CLOCK
4. DRIVER IC: ST7282-G4-1L
5. OPERATING TEMP.: -20°C ~ 70°C
6. STORAGE TEMP.: -30°C ~ 80°C
7. LED BACKLIGHT: 10-CHIP WHITE LED, If=40mA, Vf=14V~17V
8. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
9. SURFACE LUMINANCE: 550CD/M²(TYP)
10. THE SINGLE LAYER AREA OF FPC IS BENDABLE AND THE BENDING RADIUS MUST BE NOT LESS THAN 1.5MM
11. GENERAL TOLERANCE: ±0.2MM
12. ROHS COMPLIANT

MULTI-INNO TECHNOLOGY CO., LTD.

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| DRAWN BY: CAROL | 2021.04.20 | | MODULE P/N: |
| CHECKED BY: | | SCALE : 1/1 | MIO430AET-5 |
| APPROVED BY: | | UNIT: mm | DESCRIPTION: |
| VERSION NO: 01 | | SHEET NO.: 1/1 | TFT MODULE |

| VER. | REVISED DESCRIPTION | REVISOR | DATE |
|------|---------------------|---------|------------|
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